



Seminar: Programmable Networks (WS24/25) How to Read a Paper and Write a Report

Prof. Dr. Lin Wang Computer Networks Group Department of Computer Science Paderborn University



This lecture is on the key ingredients to succeed as a computer scientist.



Good coffee



Good sleep

Papers

A piece of writing to share original research work wirh other scientists and (maybe also)

non-scientists

The UNIX Time-Sharing System

Dennis M. Ritchie and Ken Thompson Bell Laboratories

operating system for the Digital Equipment Corpora-tion PDP-11/40 and 11/45 computers. It offers a number of features seldom found even in larger operating systems, including: (1) a hierarchical file system incorpo rating demountable volumes; (2) compatible file, device, and inter-process I/O; (3) the ability to initiate asynchroable on a per-user basis; and (5) over 100 subsystems nature and implementation of the file system and of the

CR Categories: 4.30, 4.32

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1. Introduction

There have been three versions of UNIX. The earliest version (circa 1969-70) ran on the Digital Equipment Cor-poration PDP-7 and -9 computers. The second version ran on the unprotected PDP-11/20 computer. This pape describes only the PDP-11/40 and /45 [1] system since it is more modern and many of the differences between it and older UNIX systems result from redesign of features found to be deficient or lacking.

Since PDP-11 UNIX became operational in February are generally smaller than the system described here. Most of them are engaged in applications such as the preparation and formatting of patent applications and other textua material, the collection and processing of trouble data from various switching machines within the Bell System, and recording and checking telephone service orders. Our own tems, languages, computer networks, and other topics in cience, and also for document preparation

Perhaps the most important achievement of UNIX is to demonstrate that a powerful operating system for interachuman effort: UNIX can run on hardware costing as little as \$40,000, and less than two man years were spent on the main system software. Yet UNIX contains a number of fea tures seldom offered even in much larger systems. It is hoped, however, the users of UNIX will find that the most important characteristics of the system are its simplicity

Besides the system proper, the major programs available under UNIX are: assembler, text editor based on QED [2], linking loader, symbolic debugger, compiler for a language resembling BCPL [3] with types and structures (C) Fortran compiler, Snobol interpreter, top-down compiler compiler (TMG) [4], bottom-up compiler-compiler (YACC), form letter generator, macro processor (M6) [5], and per

and novelty programs. All of these programs were written porting. All UNIX software is maintained under UNIX; like UNIX editor and text formatting program.

bytes of core memory; UNIX occupies 42K bytes. This sys Hall, NI 19974.

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Hence University of Core memory; UNIX occupies 42K bytes. This system to five dependent of any deviations from the original; I have left errors in the original unchange in the original unc

A Network in a Laptop: Rapid Prototyping for Software-Defined Networks

Bob Lantz Network Innovations Lab DOCOMO USA Labs Palo Alto, CA, USA

Brandon Heller Dept. of Computer Science. rlantz@cs.stanford.edu brandonh@stanford.edu

Dept. of Electrical Engineering Stanford, CA, USA

ABSTRACT

Mininet is a system for rapidly prototyping large networks on the constrained resources of a single laptop. The lightweight approach of using OS-level virtualization features, including processes and network namespaces, allow it to scale to hundreds of nodes. Experiences with our initial implementation suggest that the ability to run, poke, and debug in real time represents a qualitative change in work-flow. We share supporting case studies culled from over 100 users, at 18 institutions, who have developed Software Defined Networks (SDN). Ultimately, we think the greatest value of Mininet will be supporting collaborative net-work research, by enabling self-contained SDN prototypes which anyone with a PC can download, run, evaluate, ex-

Categories and Subject Descriptors

C.2.1 [Computer Systems Organization] Computer-Communication Networks-Network com ions; B.4.4 [Performance Analysis and

General Terms

Design Experimentation Verification

Flow emulation virtualization

1. INTRODUCTION

Inspiration hits late one night and you arrive at a world-changing idea: a new network architecture, adment should you use to evaluate your idea? With this

should be defined in software, using familiar languages and operating systems.

totype on hardware-based networks and testbeds should require no changes to code or configuration

should occur in real time, as if interacting with

Scalable: the prototyping environment should scale switches on only a laptop.

behavior with a high degree of confidence; for example, applications and protocol stacks should be usable without modification.

Share-able: self-contained prototypes should be eas-

expensive and beyond the reach of most researcher Simulators such as ns.2 [14] or Onnet [19] are annealing because they can run on a laptop, but they lack realism: the code created in the simulator is not the same code that would be deployed in the real network, ctive. At first glance, a network of virtual machines (VMs) is appealing. With a VM

Spanner: Google's Globally-Distributed Database

James C. Carbett, Jeffrey Dean, Michael Enstein, Andrew Files, Christopher Frost, II Furman Sanjay Ghemawat, Andrey Gubarev, Christopher Heiser, Peter Hochschild, Wilson Hsieh, Sebastian Kanthak, Eugene Kogan, Hongyi Li, Alexander Lloyd, Sergey Melnik, David Mwaura, David Nagle, Sean Quinlan, Rajesh Rao, Lindsay Rolig, Yasushi Saito, Michal Szymaniak, Christopher Taylor, Ruth Wang, Dale Woodford

Abstract

distributed, and synchronously-replicated database. It is the first system to distribute data at global scale and sup-port externally-consistent distributed transactions. This paper describes how Spanner is structured, its feature set. the rationale underlying various design decisions, and a novel time API that exposes clock uncertainty. This API and its implementation are critical to supporting external consistency and a variety of powerful features: non-slocking reads in the past, lock-free read-only transactions, and atomic schema changes, across all of Spanner.

Spanner is a scalable, elobally-distributed database designed, built, and deployed at Google. At the high-est level of abstraction, it is a database that shards data across many sets of Paxos [21] state machines in datanters spread all over the world. Replication is used for global availability and geographic locality; clients automatically failover between replicas. Spanner automatically reshards data across machines as the amount of data or the number of servers changes, and it automatically migrates data across machines (even across datacenters) to balance load and in response to failures. Spanner is designed to scale up to millions of machines across hun-dreds of datacenters and trillions of database rows.

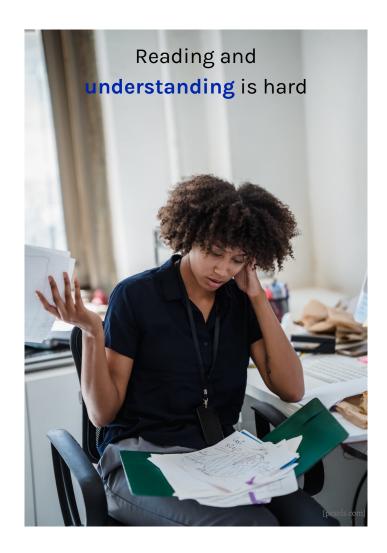
Applications can use Spanner for high availability, even in the face of wide-area natural disasters, by replicating their data within or even across continents. Our rating their data within to even actions continents. Our initial customer was F1 [35], a rewrite of Google's advertising backend. F1 uses five replicas spread across trol durability, availability, and read performance). Data treating discense. Fi uses intelligible agreement across the United States, Most other applications will probably replicate their data across 3 to 5 datacenters in one geographic region, but with relatively independent failure modes. That is, most applications will choose lower that the are difficult to implement in a distributed database: at that are difficult to implement in a distributed database; and the second of the contraction of the contr

tency over higher availability, as long as they can survive

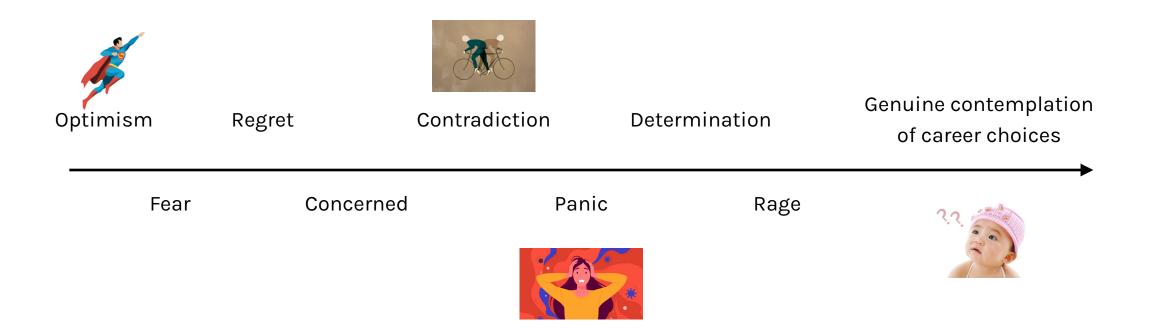
Snanner's main focus is managing cross-datacente time in designing and implementing important database features on top of our distributed-systems infrastructure Even though many projects happily use Bigtable [9], we have also consistently received complaints from users that Bigtable can be difficult to use for some kinds of ap plications: those that have complex, evolving schemas, or those that want strong consistency in the presence of wide-area replication. (Similar claims have been made by other authors [37].) Many applications at Google have chosen to use Megastore [5] because of its semi relational data model and support for synchronous repl cation despite its relatively poor write throughout. As a versioned key-value store into a temporal multi-version database. Data is stored in schematized semi-relationa cally timestamped with its commit time; old versions of data are subject to configurable garbage-collection poli-

vides a SQL-based query language. As a globally-distributed database, Spanner provides fine grain by applications. Applications can specify con how far replicas are from each other (to control write la

10th USENIX Symposium on Operating Systems Design and Implementation (OSDI '12) 25'



Stages of reading a scientific paper



Stages of reading a scientific paper



Do not worry, we have all been through these stages

Papers can be fun

Only 365 Days Left Until The Sigcomm Deadline

Jon Crowcroft, Christian Kreibich University of Cambridge Computer Laborator (firstname.lastname)@cl.cam.ac.uk

ABS) RACL Confly these hundred stay four days let useful the Signormal doubline. Only these hundred stay from the special form of the special form

Neywords

"Only," 1 - 365, "days", "left", "until", "the", "Sigcomm", deadline"

2.1 Only 320 days left until the Sigcomm deadline

1. ONLY 354 DAYS LEFT UNTIL THE SIGCOMM DEADLINE

three hundred thirty days left until the Sigcomm deadline. Only three hundred Twenty 9, days left until the Sigcomm deadline. Only three hundred Twenty 8, days left — until the Sigcomm deadline — only three hundred Twenty 7 days left until the Sigcomm deadline — only three hundred Twenty 7 days left until the Sigcomm deadline. Only three hundred Twenty 6 days left until the Sigcomm deadline. Only three hundred Twenty 6 days left until the Sigcomm deadline.

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2.2 Only 312 days left until the Sigcomm deadline
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Can $n^2 + 1$ unit equilateral triangles cover an equilateral triangle of side > n, say $n + \varepsilon$? John H. Conway , Alexander Soifer Princeton University, Mathematics, Fine Hall, Princeton, NJ 08544, US Figure 2:

IEEE TRANSACTIONS ON AUTOMATIC CONTROL, VOL. AC-23, NO. 4, AUGUST 1978

Guaranteed Margins for LQG Regulators

JOHN C. DOYLE

Abstract-There are none.

Bulletin of the Seismological Society of America

Vol. 64 October 1974 No. 5

IS THE SEQUENCE OF EARTHQUAKES IN SOUTHERN CALIFORNIA, WITH AFTERSHOCKS REMOVED, POISSONIAN?

By J. K. GARDNER and L. KNOPOFF

ABSTRACT

Yes.

For big fans of reading short papers



Tiny Transactions on Computer Science (TinyToCS) is the premier venue for computer science research of 140 characters or less.

<u>Index</u> | <u>Organizers</u> | <u>Contact</u> | <u>Archived CFP</u> | <u>Fork</u>

Volume 4 Index



Research and Review Methodology: The Way Our Community Continues to Thrive

 I Aver: Providing Declarative Experiment Specifications Facilitates the Evaluation of Computer Systems Research

Providing declarative statements that describe the outcome of an experiment can significantly improve the task of validating its results.

Ivo Jimenez (UC Santa Cruz)

Carlos Maltzahn (UC Santa Cruz)

Jay Lofstead (Sandia National Laboratories)

Adam Moody (Lawrence Livermore National Laboratory)

Kathryn Mohror (Lawrence Livermore National Laboratory) Remzi Arpaci-Dusseau (University of Wisconsin-Madison)

Andrea Arpaci-Dusseau (University of Wisconsin-Madison)

It is More Blessed to Give than to Receive - Open Software Tools Enable Open Innovation
 Sharing software tools enables open innovation, brings faster upgrades and frees up resources, but demands investments in the open community.

Per Runeson (Lund University)

Hussan Munir (Lund University)

Krzysztof Wnuk (Blekinge Institute of Technology)

• The Full Mont(olog)y: Using Ontologies to Optimize the CS Literature Review

We need a comprehensive and maintainable ontology of CS research to drive scientific progress despite massive knowledge availability.

Kathryn Dahlgren (UC Santa Cruz)

Papers are fun

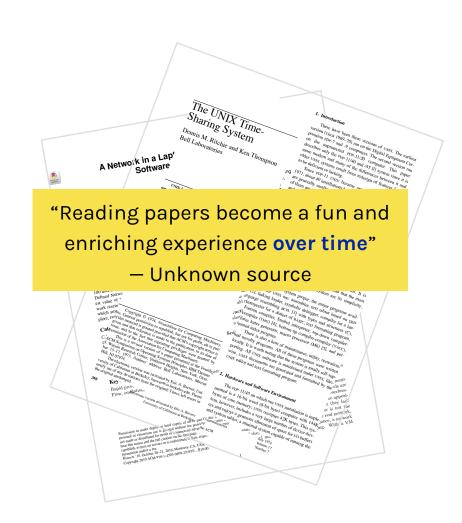
Written with a specific audience in mind

Context or background is key

- Richard Feynman answering a question on magnetism: https://www.youtube.com/watch?v=M00r930Sn_8

Strengthen your background

- Read voraciously
- No shortcuts!



Prepare

Environment

- Place: cafe, library, ...
- Ambience: music, quiet, ...
- Medium: printout, tablet, laptop, ...

Set expectations

- Presentation?
- Critique?
- Leisurely read?



Start by reading a paper

How to Read a Paper

S. Keshav
David R. Cheriton School of Computer Science, University of Waterloo
Waterloo, ON, Canada
kashav@uwaterloo.ca

ABSTRACT

Researchers spend a great deal of time reading research papers. However, this skill is rarely taught, leading to much wasted effort. This article outlines a practical and efficient three-pass method for reading research papers. I also describe how to use this method to do a literature survey. Categories and Subject Descriptors: A.1 [Introductory

General Terms: Documentation.
Keywords: Paper, Reading, Hints.

1. INTRODUCTION

Researchers must read papers for several reasons: to review them for a conference or a class, to keep current in their field, or for a literature survey of a new field. A typical researcher will likely spend hundreds of hours every year reading papers.

Learning to efficiently read a paper is a critical but rarely taught skill. Beginning graduate students, therefore, must learn on their own using trial and error. Students waste much effort in the process and are frequently driven to frustration.

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2. THE THREE-PASS APPROACH

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2.1 The first pass

The first pass is a quick scan to get a bird's-eye view of the paper. You can also decide whether you need to do any more passes. This pass should take about five to ten minutes and consists of the following steps:

- 1. Carefully read the title, abstract, and introduction
- 2. Read the section and sub-section headings, but ignore everything else
- 3. Read the conclusions

 Glance over the references, mentally ticking off the ones you've already read
 At the end of the first pass, you should be able to answer

 Category: What type of paper is this? A measurement paper? An analysis of an existing system? A

- description of a research prototype?
- Context: Which other papers is it related to? Which theoretical bases were used to analyze the problem?
- Correctness: Do the assumptions appear to be valid:
- 4. Contributions: What are the paper's main contributions?
- Clarity: Is the paper well written?

Using this information, you may choose not to read further. This could be because the paper doesn't interest you, or you don't know enough about the area to understand the paper, or that the authors make invalid assumptions. The first pass is adequate for papers that aren't in your research area, but may someday prove relevant.

Incidentally, when you write a paper, you can expect most reviewers (and readers) to make only one pass over it. Take care to choose coherent section and sub-section titles and to write concise and comprehensive abstracts. If a reviewer cannot understand the gist after one pass, the paper will likely be rejected; if a reader cannot understand the highlights of the paper after five minutes, the paper will likely never be read.

2.2 The second pass

In the second pass, read the paper with greater care, but ignore details such as proofs. It helps to jot down the key points, or to make comments in the margins, as you read.

- Look carefully at the figures, diagrams and other illustrations in the paper. Pay special attention to graphs.
 Are the axes properly labeled? Are results shown with error bars, so that conclusions are statistically significant? Common mistakes like these will separate rushed, shodly work from the truly excellent.
- Remember to mark relevant unread references for further reading (this is a good way to learn more about the background of the paper).

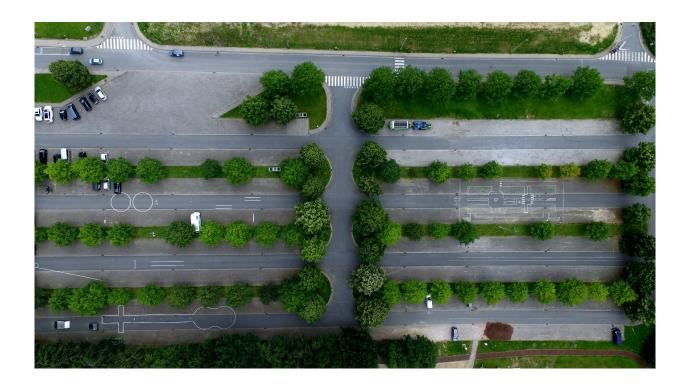
ACM SIGCOMM Computer Communication Review

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Volume 37, Number 3, July 2007

First pass

Obtain a bird's eye view of the paper

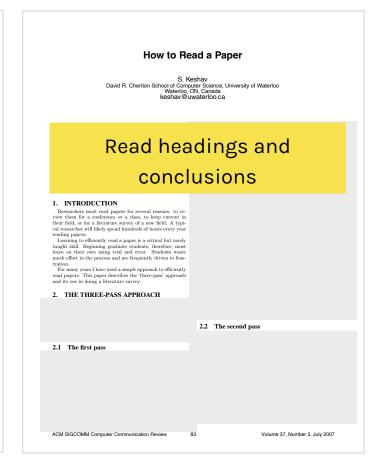


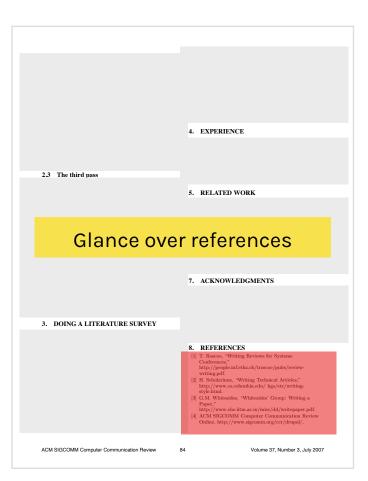
First pass

ACM SIGCOMM Computer Communication Review

How to Read a Paper S. Keshav David R. Cheriton School of Computer Science, University of Waterloo Waterloo, ON, Canada keshav@uwaterloo.ca ABSTRACT 4. Glance over the references, mentally ticking off the ones vou've already read At the end of the first pass, you should be able to answer the five Cs: Category: What type of paper is this? A measure-ment paper? An analysis of an existing system? A description of a research prototype? Categories and Subject Descriptors: A.1 [Introductory General Terms: Documentation. 2. Context: Which other papers is it related to? Which Keywords: Paper, Reading, Hints. theoretical bases were used to analyze the problem? 1. INTRODUCTION $3. \ \ Correctness: \ Do \ the \ assumptions \ appear \ to \ be \ valid?$ Researchers must read papers for several reasons: to review them for a conference or a class, to keep current in their field, or for a literature survey of a new field. A typical researcher will likely spend hundreds of hours every year 4. Contributions: What are the paper's main contribu-5. Clarity: Is the paper well written? reading papers. Learning to efficiently read a paper is a critical but rarely taught skill. Beginning graduate students, therefore, must learn on their own using trial and error. Students waste much effort in the process and are frequently driven to frus-Using this information, you may choose not to read further. This could be because the paper doesn't interest you. or you don't know enough about the area to understand the paper, or that the authors make invalid assumptions. The first pass is adequate for papers that aren't in your research area, but may someday prove relevant. Incidentally, when you write a paper, you can expect most reviewers (and readers) to make only one pass over it. Take care to choose coherent section and sub-section titles and to write concise and comprehensive abstracts. If a reviewer cannot understand the gist after one pass, the paper will 2. THE THREE-PASS APPROACH Carefully read the title, abstract, and introduction 1. Carefully read the title, abstract, and introduction rushed, shoddy work from the truly excellent. Read the section and sub-section headings, but ignore everything else 2 Remember to mark relevant unread references for further reading (this is a good way to learn more about 3 Read the conclusions the background of the paper).

Volume 37, Number 3, July 2007





Second pass

Read with greater care, but ignore details

How to Read a Paper

S. Keshav
David R. Cheriton School of Computer Science, University of Waterloo
Waterloo, ON, Canada

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ABS/IKACI
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For many years I have used a simple approach to efficiently read papers. This paper describes the 'three-pass' approach and its use in doing a literature survey.

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Volume 37, Number 3, July 2007

Take notes or jot down points; up to an hour

Third pass

Virtually re-implement the paper

- Appreciate innovations
- Identify shortcomings
- Can take 4-5 hours for beginners
- Up to an hour for experienced readers

How to Read a Paper

S. Keshav
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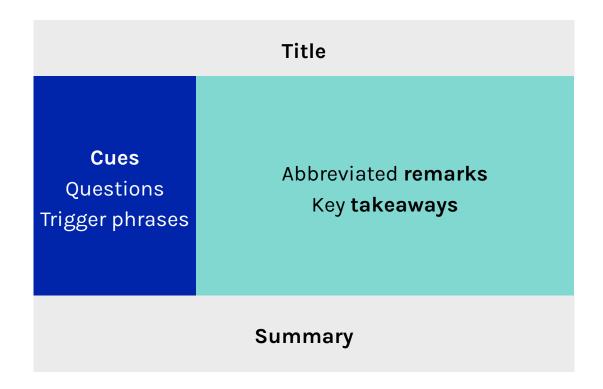
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Volume 37, Number 3, July 2007

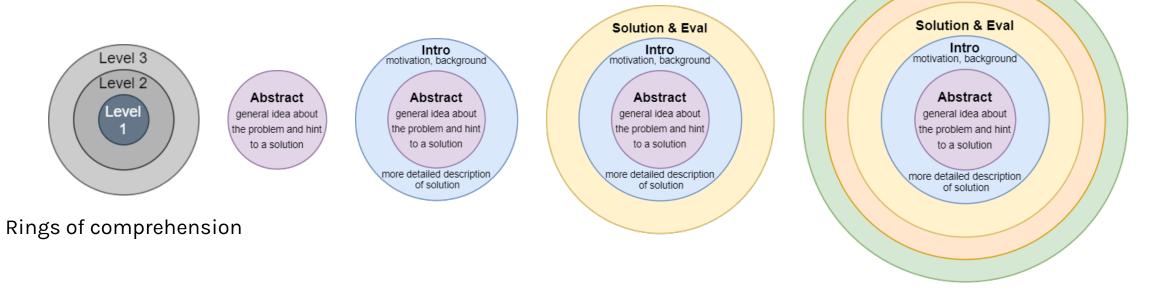
Taking notes

The cornell note-taking system



The shampoo algorithm

Lather, rinse, and repeat



Writing as a reading tool: our brain is too fast when we read; slow it down

Proofs Algorithms

How to find (good) papers to read

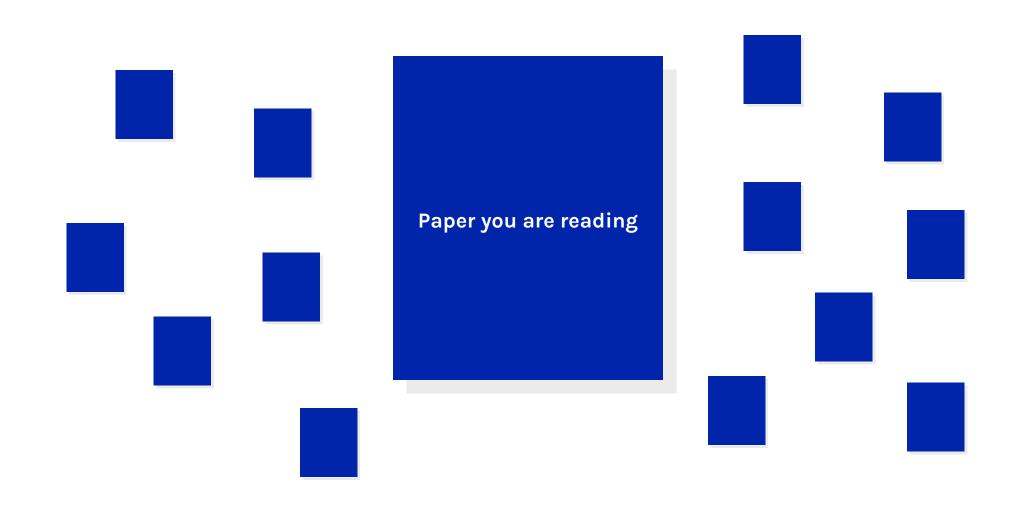
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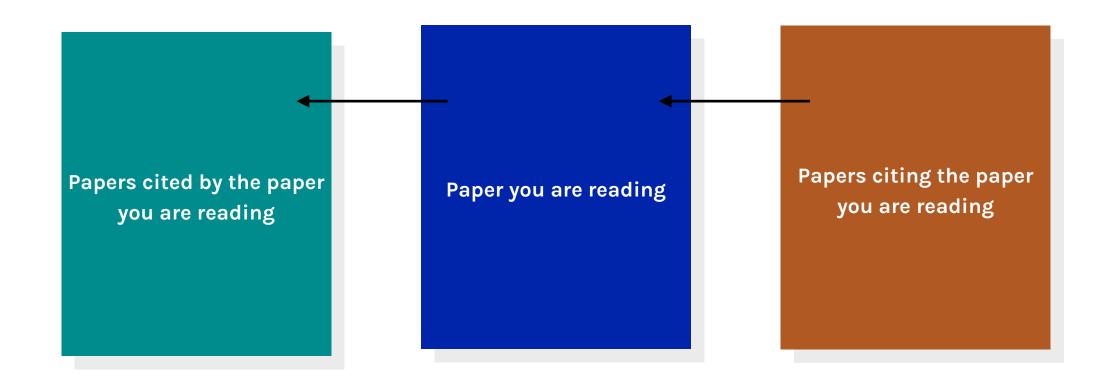


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How to find relevant papers?



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ICS '23. June 21-23. 2023. Orlando. FL. USA REFERENCES [22] F. Hauser et al. 2021. A Survey on Data Plane Programming with P4: Fundamen O. Arap and M. Swany. 2016. Offloading Collective Operations to Programmable Logic on a Zynq Cluster. In 2016 IEEE 24th Annual Symposium on High-Performance Interconnects (HOTI). 76–83.
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milliseconds for PPI, Citeseer, Pubmed, Ogbn-products, and Ogbnmag, respectively. These overheads are negligible compared to total execution time (Figure 7) for most datasets Ogbn-products. The overhead is similar to that of setting up non-FLASH versions in distributed computing systems (since data is not always available in the corresponding nodes).

5.9 Comparison with Prior Work

To demonstrate the applicability of the approach to other applications, and compare it with other in-switch computing approaches, we consider DNN training on FLASH and Mellanox SHArP [14] We compare the time it takes to update the (last-layer) weights of the AlexNet model during DNN training using FLASH to that of doing so on the same Stampede2 cluster using Mellanox switches. Our simulation results show that FLASH achieves 1.7× speedup on

iteration. Weight updates for all layers except the last are performed

in the same way as in the baseline (synchronous Allreduce-based

training [29]). Instead of communicating and transferring weights

back and forth to the nodes (Allreduce for the current iteration),

performing computation on them (forward propagation for the

next iteration), and then another communication (Allreduce for the

next iteration), weights are processed in the switches, resulting in

reduced communication time. We note that it is not possible to take

advantage of Mellanox offload support for GCN inference as these

In-switch collective processing: Previous work has shown significant benefits of optimizing collectives and offloading them to the switch. Mellanox [13] has offloaded MPI collectives to ASIC-based

are provided. The authors in [30] propose an FPGA-based in-switch

[18, 19, 46] a new method for supporting MPI communicators and accelerating collectives in the reconfigurable switches is presented

architecture for in-network data reduction. Although it is possible

to process custom operations through packet handlers, their evalu-ation is only limited to dense/sparse MPI_Allreduce. These are all

based on RISC-V cores the largest memory footprint is 4 MBytes.

switches do not support Allgather collectives

6 RELATED WORK

ICS '23, June 21-23, 2023, Orlando, FL, USA

a software and hardware-based prototype for mapping trained non-neural network ML models to switch match action pipelines

rior art utilized a CGRA closely coupled with a CPU. For instance ADRES [31] proposed a novel compiler-friendly architecture that is tightly coupled with a very long instruction word (VLIW) processor

ications to a larger number

mance advantages (for large c). We also expect FLASH to

improve the performance and scalability of other communication intensive applications as it is generic enough to support different workloads and it directly improves the communication time through in-switch computing. Some extensions are in progress. Vector PEs are pipelined together and are independent from each other except that incoming streaming packets are the same. Other types of dependencies and more complex types are not yet supported. Finally, certain parallel patterns (e.g., breadth first search) may not map efficiently to the current FLASH architecture; in future work we seek to make FLASH more general purpose.

In this work, we designed, implemented, and evaluated a programmable look-aside accelerator that can be embedded into, or attached to, existing communication switches. To facilitate usability, we developed a software toolchain to compile user-provided code for configuring the switch. While our approach is generic and supports a variety of workloads, we consider graph convolutional network (GCN) inference as a case study. Experimental results show that this approach improves both performance and scalability. The performance advantage is on average 3.4× (across five real-world datasets) on 24 nodes. As part of future work, we will demonstrate our approach for GCN training and other workloads with a larger

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References section provides the list, while the related

work section provides the context!

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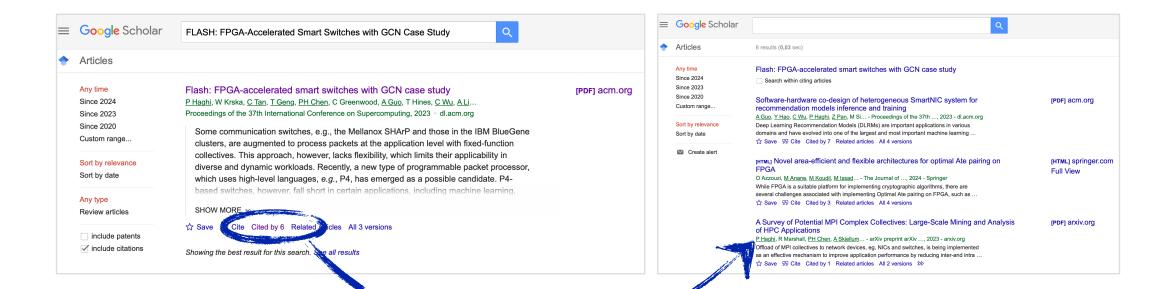
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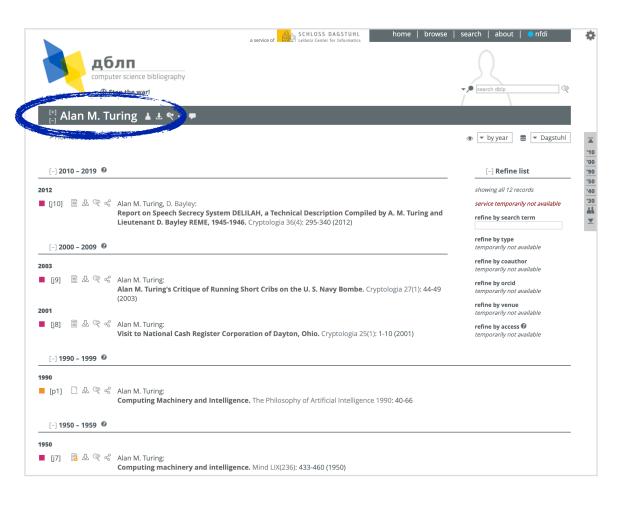
MPI Reduce with FPGAs in the Network. In Workshop on Exascale MPI

Papers citing the paper you are reading

Usually this means more recent papers...



How to find all papers from a particular author?



How to discuss a paper

Discussing a paper: peel the onion

Top level

- What is the cool idea?
- Why does it matter?
- How, when, where, and why does it work?

What questions do we have about the paper?



Discussing a paper: peel the onion

Mid level

- What are the assumptions? How is the work scoped?
- What is the evaluation?
 - Setup, workload, choice of experiments
- Does the evaluation support the claims?
- Does the paper do a good job with related work (including existing systems)?
- Did you enjoy reading the paper? Are you excited about the ideas?



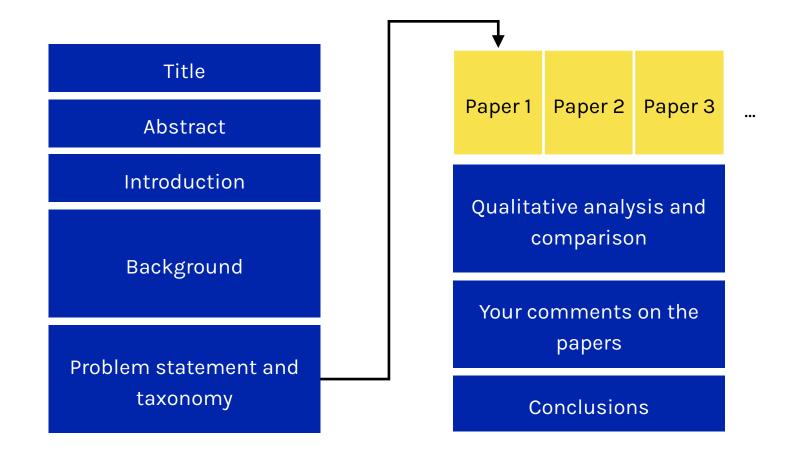
Discussing a paper: peel the onion

The heart of the discussion

- How does the work advance the state-of-the-art?
- What are the paper's strengths and limitations?
- Will it have a big impact? If so, how?
 - For older papers, does it stand the test of time?
- How does this paper stack up against reality?
 - Is the work applicable in the real world?
 - Do other systems solve the same problem differently?



Basic structure of your report





Title of the Document in One Line

Author of the Document

1 Section Heading

1.1 Subsection Heading

1.1.1 Subsubsection Heading (Avoid Using It If Possible)

Lorem Ipsum is simply dummy text of the printing and typesetting industry. Lorem Ipsum has been the industry's standard dummy text ever since the 1500s, when an unknown printer took a galley of type and scrambled it to make a type specimen book. It has survived not only five centuries, but also the leap into electronic typesetting, remaining essentially unchanged. It was popularised in the 1960s with the release of Letraset sheets containing Lorem Ipsum passages, and more recently with desktop publishing software like Aldus PageMaker including versions of Corem Ipsum.

The above shows a normal paragraph for this document. By default, the paragraph is not indented. If you want to cite a reference, you can use the \cite command. Here is an example: HIRE is a novel resource scheduler for in-network computing [1]. The list of references is shown at the end of the document in the "References"

A template will be provided: 10-20 pages.

- First iten
- Second item
- Third item
- Last item
 First subitem
- Second subitem
- Second subit
- First entry
 Second entry
- Second entry
 Third entry
- Third entry
- a. First subentry
- b. Second subentry

If you have some text you want to put in monospace (e.g., cite something in verbatim), you can use the \verb command to do that. Alternatively, you can use \\minitialine{...}\{...\}. The difference is that the latter is highlighted with a light gray background and we can also turn on syntax highlighting for many programming or scripting languages. Here is an example to compare these two: exit 0 and exit 0. For this reason, the latter is always preferred when it comes to code.

If you want to write a code block, you can use the <code>minted</code> environment, where you can turn on the syntax highlighting if you want. Here is an example for a shell script.

echo "Hello world!"

The following is an example for a C code snippet.

int main(int argc, char** argv) {
 return 0;
}

1/2

Questions?